

650V 7A Advanced N-Ch Power MOSFET

FEATURES

- Low drain-source On resistance : $R_{DS(on)} \leq 1.4\Omega$ (Typ.)
- Low gate charge : $Q_g = 21nC$ (Typ.)
- Low reverse transfer capacitance : $C_{rss} = 5pF$ (Typ.)
- Lower EMI Noise
- RoHS compliant device
- 100% avalanche tested

Marking Information

Row 1 : AP

Row 2 : Product Information

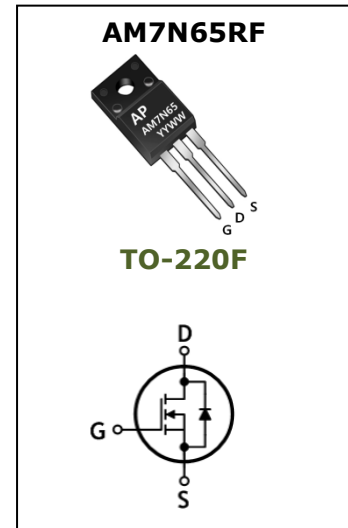
Row 3 : Date Code

- YY : Year Code

- WW : Week Code

Ordering Information

Part No	Package	Packing	Finish	Halogen	Packing Unit
AM7N65RF	TO-220F	Tube	Sn	Free	5,000ea



Maximum Ratings (T_c=25 °C, unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V _{DSS}	650	V
Gate-source voltage	V _{GSS}	±30	V
Drain current (DC)*	I _D	T _C = 25°C	7
		T _C = 100°C	4.43
Drain current (Pulsed)*	I _{DM}	28	A
Power Dissipation – TO220F	P _D	48	W
Avalanche current (Single) (Note 2)	I _{AS}	7	A
Single pulsed avalanche energy (Note 2)	E _{AS}	530	mJ
Avalanche current (Repetitive) (Note 1)	I _{AR}	7	A
Repetitive avalanche energy (Note 1)	E _{AR}	3.2	mJ
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55~150	

*Limited only maximum junction temperature

Thermal Characteristic

Characteristic	Symbol	Rating	Unit
Thermal resistance, junction to case	$R_{th(j-c)}$	Max. 2.6	°C/W
Thermal resistance, junction to ambient	$R_{th(j-a)}$	Max. 62.5	

Electrical Characteristics ($T_c=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DS}	$I_D = 250\mu\text{A}$, $V_{GS} = 0$	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$I_D = 250\mu\text{A}$, $V_{DS} = V_{GS}$	3	-	5	V
Drain-source cut-off current	I_{DSS}	$V_{DS} = 650\text{V}$, $V_{GS} = 0\text{V}$	-	-	1	μA
		$V_{DS} = 650\text{V}$, $T_c = 150^{\circ}\text{C}$	-	-	100	
Gate leakage current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 30\text{V}$	-	-	± 100	nA
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 3.5\text{A}$	-	1.2	1.4	Ω
Forward transfer conductance (Note 3)	g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 3.5\text{A}$	-	8.7	-	S
Input capacitance	C_{iss}	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1.0\text{MHz}$	-	1380	-	pF
Output capacitance	C_{oss}		-	90	-	
Reverse transfer capacitance	C_{rss}		-	9	-	
Turn-on delay time (Note 3,4)	$T_{d(on)}$	$V_{DS} = 325\text{V}$, $I_D = 7\text{A}$, $R_G = 25\Omega$	-	94	-	ns
Rise time (Note 3,4)	T_r		-	89	-	
Turn-off delay time (Note 3,4)	$t_{d(off)}$		-	182	-	
Fall time (Note 3,4)	t_f		-	53	-	
Total gate charge (Note 3,4)	Q_g	$V_{DS} = 520\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 7\text{A}$	-	21	26	nC
Gate-source charge (Note 3,4)	Q_{gs}		-	9	-	
Gate-drain charge (Note 3,4)	Q_g		-	5	-	

Source-Drain Diode Ratings and Characteristics ($T_c=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Source current (DC)	I_S	Integral reverse diode in the MOSFET	-	-	7	A
Source current (Pulsed)	I_{SM}		-	-	28	A
Forward voltage	V_{SD}	$V_{GS} = 0\text{V}$, $I_S = 7\text{A}$	-	-	1.4	V
Reverse recovery time (Note 3,4)	t_{rr}	$I_S = 7\text{A}$, $V_{GS} = 0\text{V}$ $dI_F/dt = 100\text{A}/\mu\text{s}$	-	474	-	ns
Reverse recovery charge (Note 3,4)	Q_{rr}		-	2.4	-	μC

Note :

1. Repetitive rating : Pulse width limited by maximum junction temperature.
2. $L=20\text{mH}$, $I_{AS}=7\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$
3. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature



Typical Electrical Characteristic

Fig. 1 Typical Output Characteristics

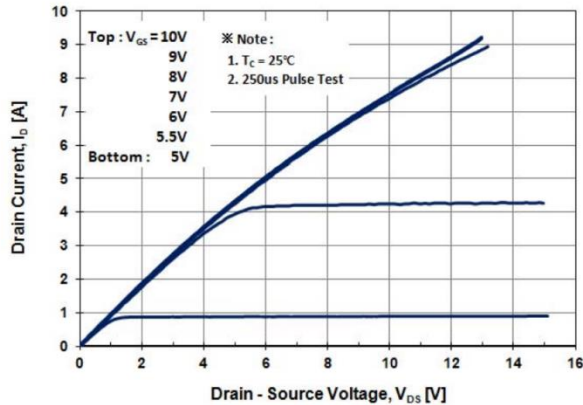


Fig. 2 Typical Output Characteristics

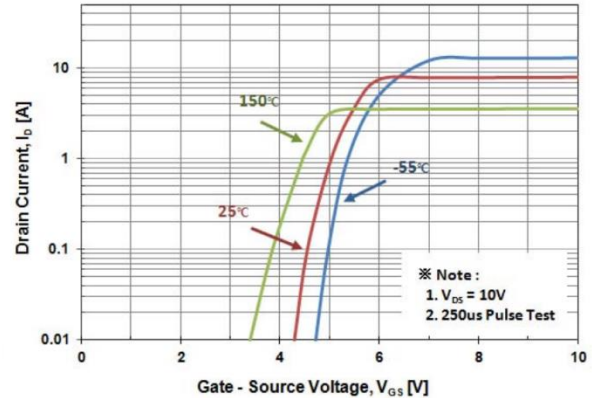


Fig.3 On-Resistance Variation with Drain Current and Gate Voltage

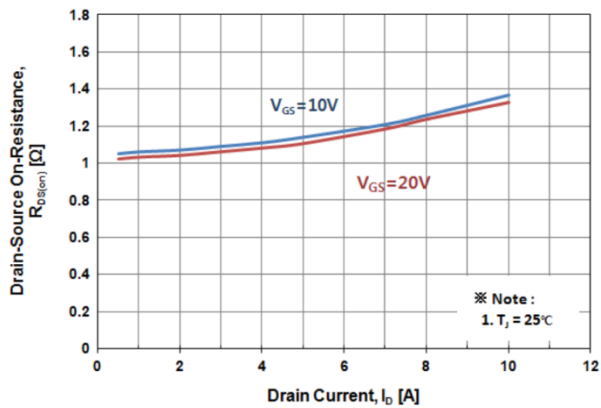


Fig. 4 Body Diode Forward Voltage Variation with Source Current

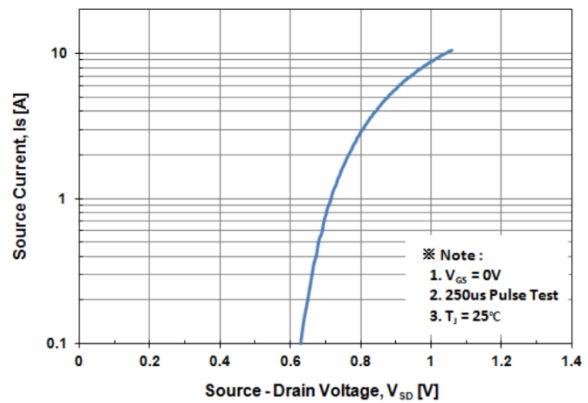


Fig. 5 Typical Capacitance Characteristics

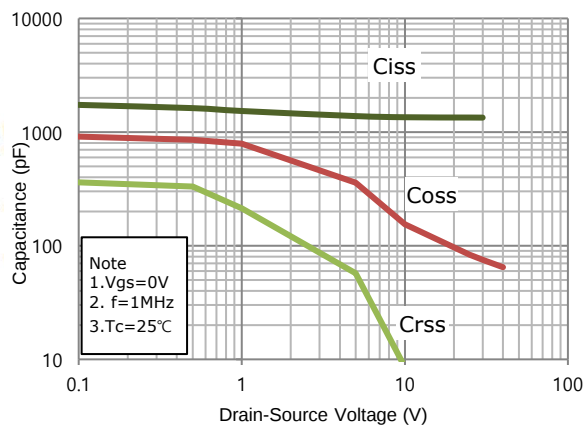
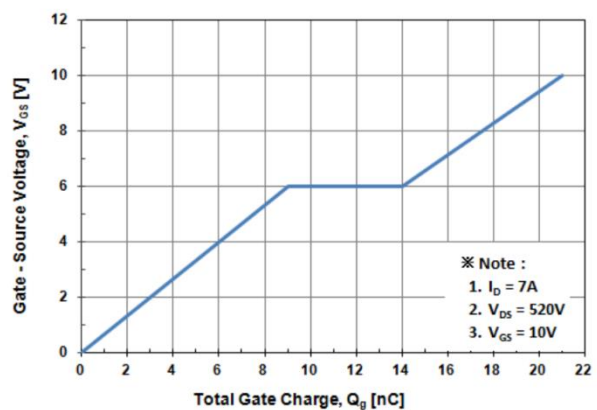


Figure 5 : Capacitance - Vds

Fig. 6 Typical Total Gate Charge Characteristics





Typical Electrical Characteristic

Fig. 7 Breakdown Voltage Variation vs. Temperature

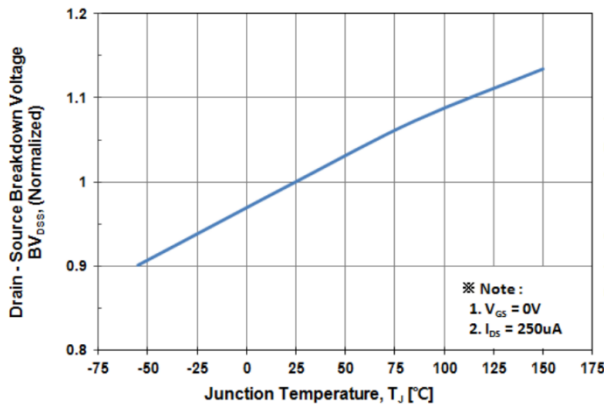


Fig. 8 On-Resistance Variation vs. Temperature

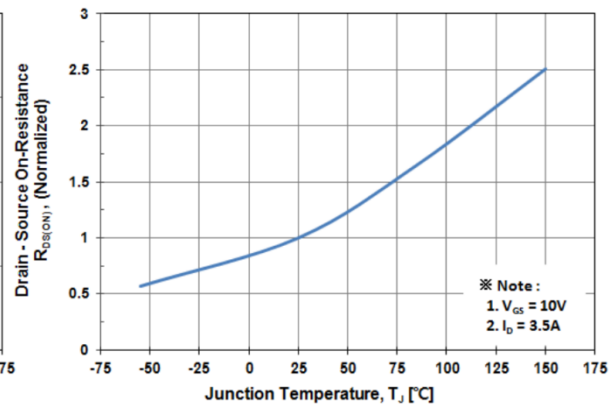


Fig. 9 Maximum Drain Current vs. Case Temperature

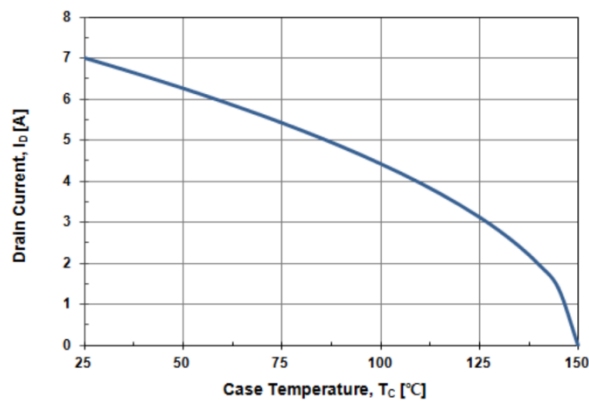


Fig. 10 Maximum Safe Operating Area

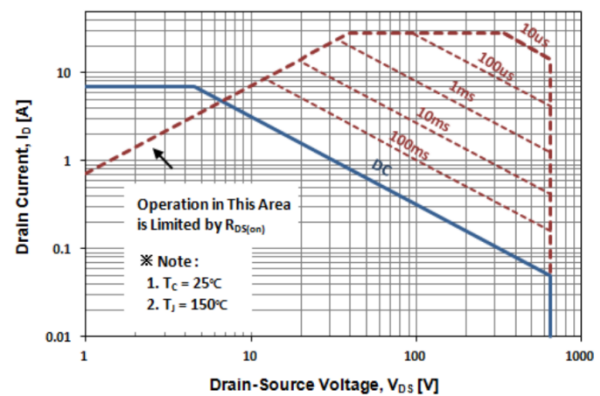


Fig. 11 Transient Thermal Impedance

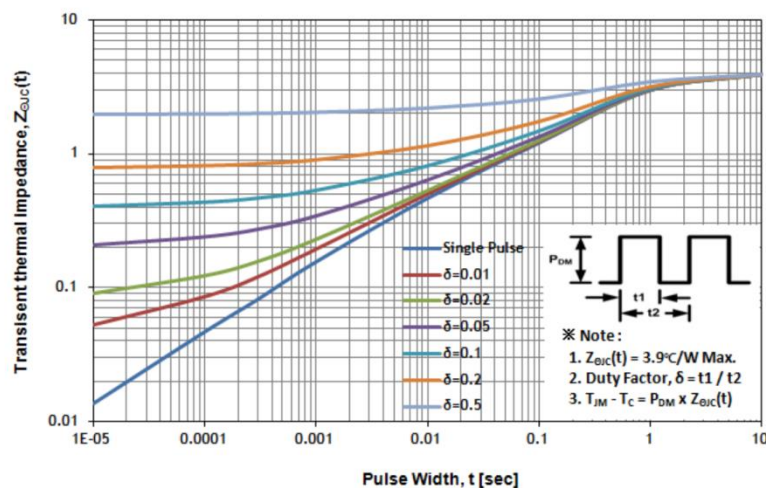




Fig. 12 Gate Charge Test Circuit & Waveform

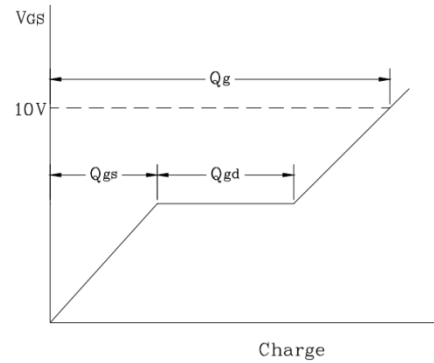
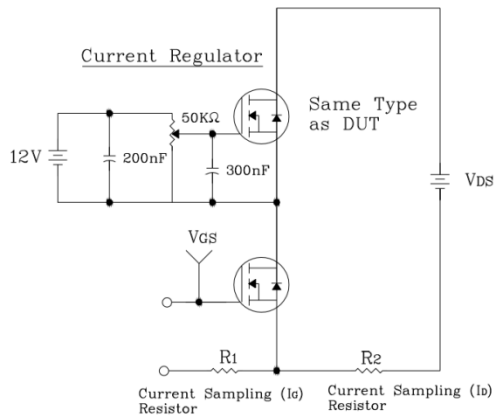


Fig. 13 Resistive Switching Test Circuit & Waveform

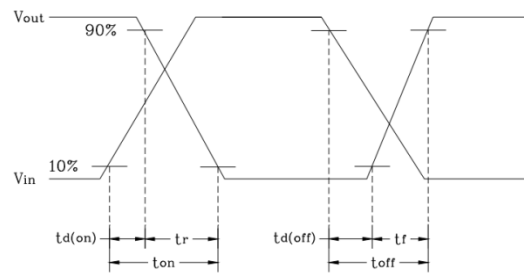
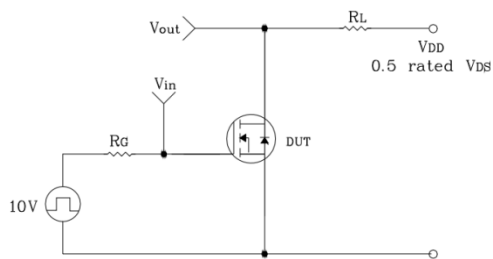


Fig. 14 EAS Test Circuit & Waveform

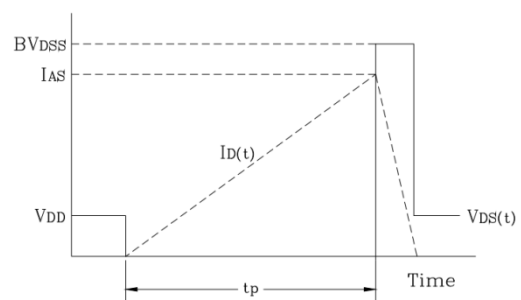
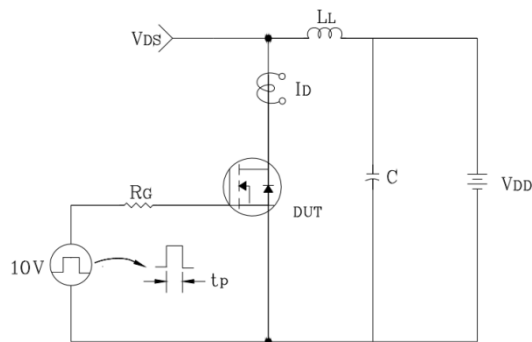
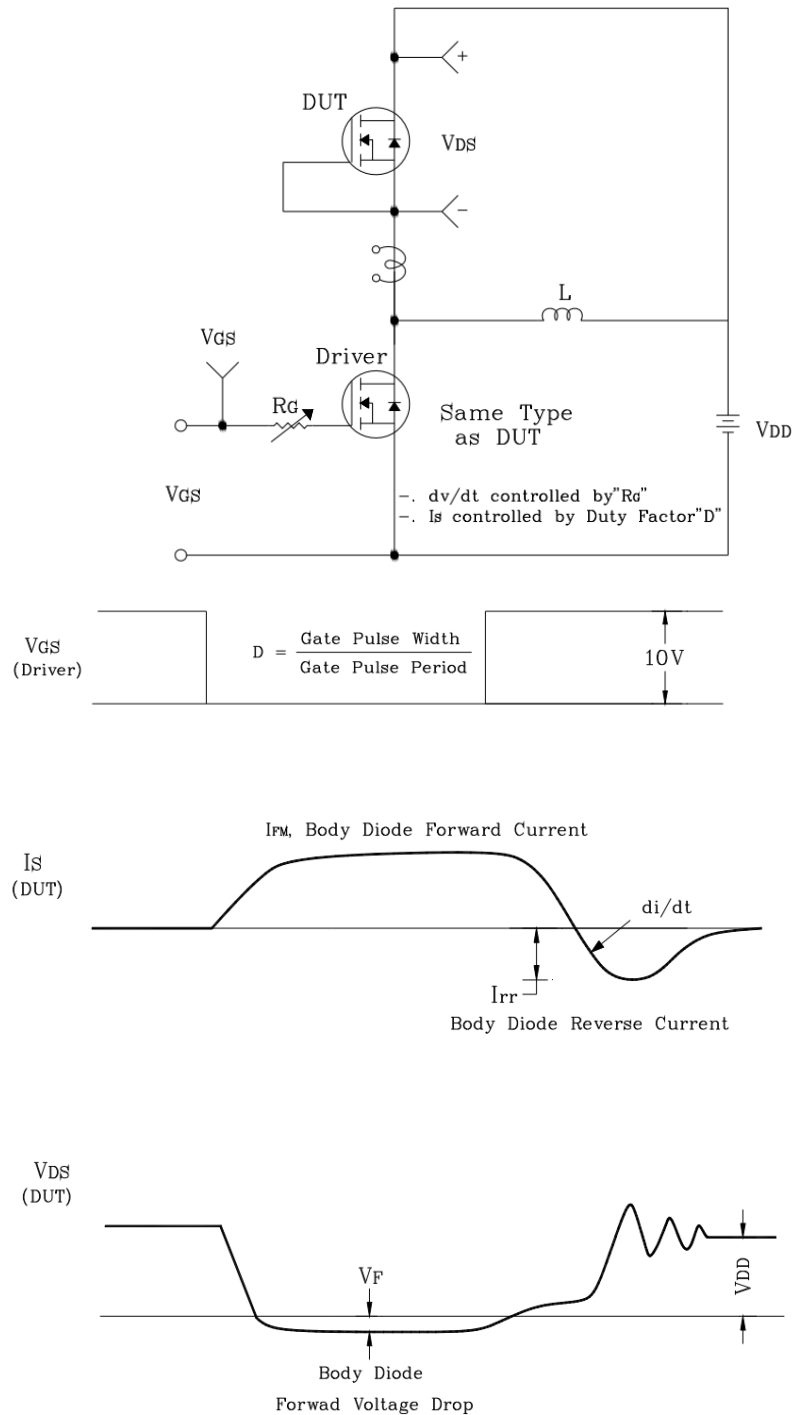




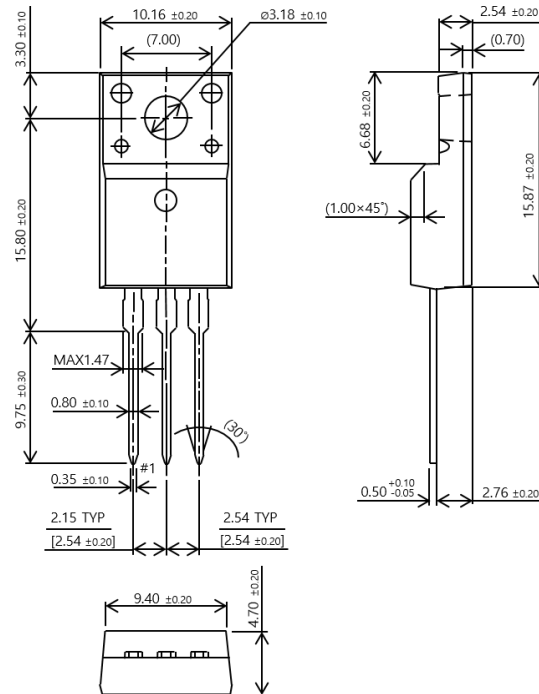
Fig. 15 Diode Reverse Recovery Time Test Circuit & Waveform



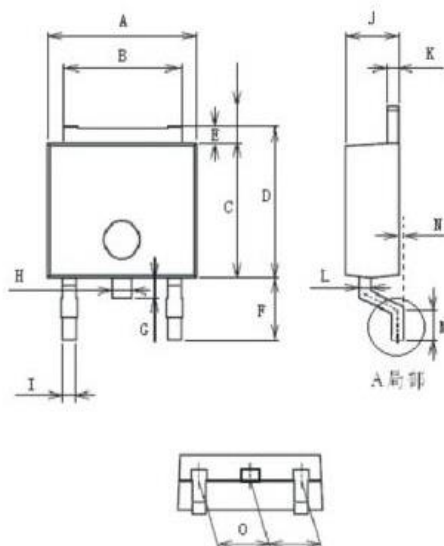


Package Dimensions

TO-220F



TO-252



Unit: mm

Item	Min	Max
A	6.40	6.70
B	5.20	5.40
C	6.00	6.30
D	6.55	6.85
E	0.45	0.60
F	3.07	3.35
G	0.85	1.05
H	0.75	0.95
I	0.55	0.75
J	2.20	2.40
K	0.43	0.58
L	0.43	0.58
M	0.90	1.10
N	0.90	1.10
O	2.20	2.40

Revision History

No	Date	Contents
0	2016-09-17	Initial Brief Datasheet Release
1	2019-01-11	Capacitance Update
2	2019-03-22	R _{θJC} , P _D Update

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