

500V 4.5A Advanced N-Ch Power MOSFET

FEATURES

- High Voltage : $BV_{DSS}=500V(\text{Min.})$
- Low C_{rss} : $C_{rss}=8pF(\text{Typ.})$
- Low gate charge : $Q_g=13nC(\text{Typ.})$
- Low $R_{DS(on)}$: $R_{DS(on)}=1.3\Omega(\text{Typ.})$
- Lower EMI noise
- 100% Avalanche tested

Marking Information

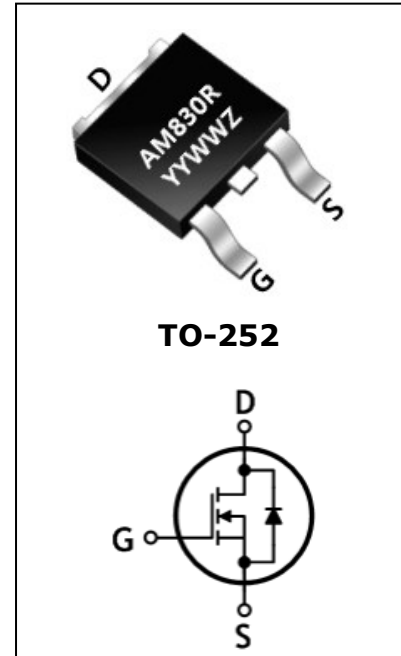
Row 1 : AP

Row 2 : Product Information

Row 3 : Date Code

- YY : Year Code

- WW : Week Code



Ordering Information

Part No	Package	Packing	Finish	Halogen	Packing Unit
AM830RD	TO-252 (DPAK)	13" Reel	Sn	Free	2,500ea

Maximum Ratings (Tc=25 °C, unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	500	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current (DC)*	I_D	$T_C = 25^\circ C$	4.5
		$T_C = 100^\circ C$	2.85
Drain current (Pulsed)*	I_{DM}	18	A
Power Dissipation – TO251	P_D	48	W
Avalanche current (Single) (Note 2)	I_{AS}	4.5	A
Single pulsed avalanche energy (Note 2)	E_{AS}	281	mJ
Avalanche current (Repetitive) (Note 1)	I_{AR}	4.5	A
Repetitive avalanche energy (Note 1)	E_{AR}	4.8	mJ
Junction temperature	T_J	150	$^\circ C$
Storage temperature range	T_{stg}	-55~150	

*Limited only maximum junction temperature

Thermal Characteristic

Characteristic	Symbol	Rating	Unit
Thermal resistance, junction to case	$R_{th(j-c)}$	Max. 2.6	$^{\circ}\text{C/W}$
Thermal resistance, junction to ambient	$R_{th(j-a)}$	Max. 62.5	

Electrical Characteristics ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DS}	$I_D = 250\mu\text{A}$, $V_{GS} = 0$	500	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$I_D = 250\mu\text{A}$, $V_{DS} = V_{GS}$	2	-	4	V
Drain-source cut-off current	I_{DSS}	$V_{DS} = 500\text{V}$, $V_{GS} = 0\text{V}$	-	-	1	μA
Gate leakage current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 30\text{V}$	-	-	± 100	nA
Drain-source on-resistance (Note 4)	$R_{DS(ON)}$	$V_{GS} = 10\text{V}$, $I_D = 2.25\text{A}$	-	1.3	1.5	Ω
Forward transfer conductance (Note 4)	g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 2.25\text{A}$	-	4.5	-	S
Input capacitance	C_{iss}	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1.0\text{MHz}$	-	790	-	pF
Output capacitance	C_{oss}		-	60	-	
Reverse transfer capacitance	C_{rss}		-	8	-	
Turn-on delay time (Note 3,4)	$T_{d(on)}$	$V_{DS} = 250\text{V}$, $I_D = 4.5\text{A}$, $R_G = 25\Omega$	-	44	-	ns
Rise time (Note 3,4)	T_r		-	80	-	
Turn-off delay time (Note 3,4)	$t_{d(off)}$		-	163	-	
Fall time (Note 3,4)	t_f		-	51	-	
Total gate charge (Note 3,4)	Q_g	$V_{DS} = 400\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 4.5\text{A}$	-	13.5	18	nC
Gate-source charge (Note 3,4)	Q_{gs}		-	5	-	
Gate-drain charge (Note 3,4)	Q_g		-	2.5	-	

Source-Drain Diode Ratings and Characteristics ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Source current (DC)	I_S	Integral reverse diode in the MOSFET	-	-	4.5	A
Source current (Pulsed) (Note 1)	I_{SM}		-	-	18	A
Forward voltage (Note 4)	V_{SD}	$V_{GS} = 0\text{V}$, $I_S = 9\text{A}$	-	-	1.4	V
Reverse recovery time (Note 3, 4)	t_{rr}	$I_S = 9\text{A}$, $V_{GS} = 0\text{V}$ $dI_F/dt = 100\text{A}/\mu\text{s}$	-	330	-	ns
Reverse recovery charge (Note 3, 4)	Q_{rr}		-	1.15	-	μC

Note :

1. Repetitive rating : Pulse width limited by maximum junction temperature.
2. $L=25\text{mH}$, $I_{AS}=4.5\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$
3. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
4. Essentially independent of operating temperature

Typical Electrical Characteristic Curves

Fig. 1 Typical Output Characteristics

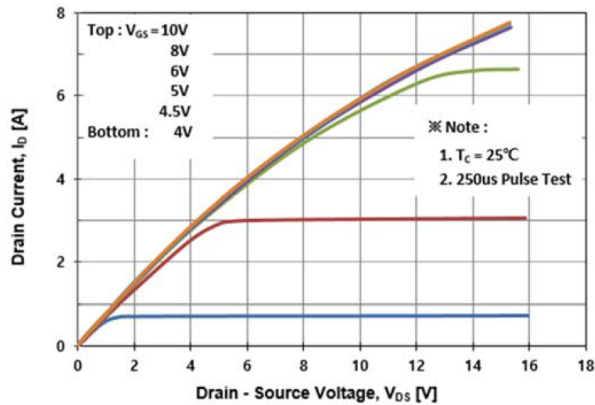


Fig. 2 Typical Output Characteristics

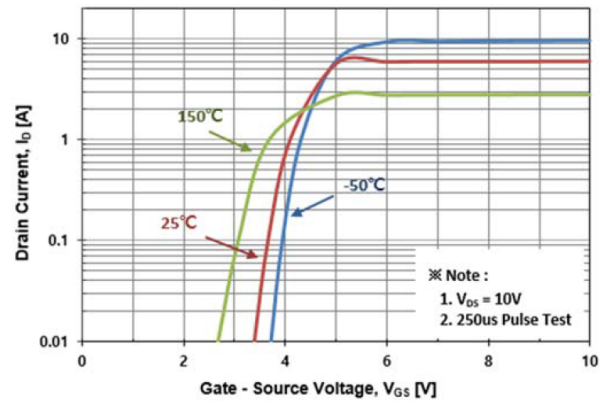


Fig.3 On-Resistance Variation with Drain Current and Gate Voltage

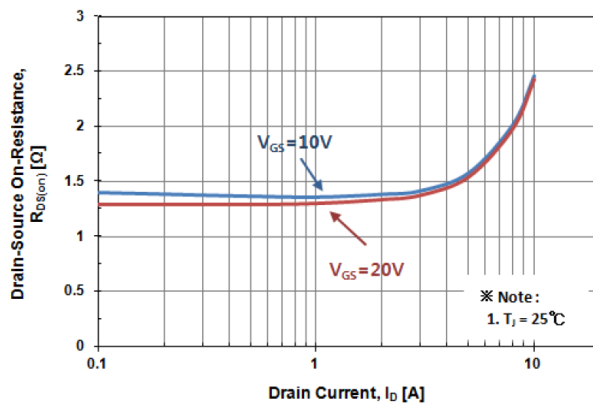


Fig. 4 Body Diode Forward Voltage Variation with Source Current and Temperature

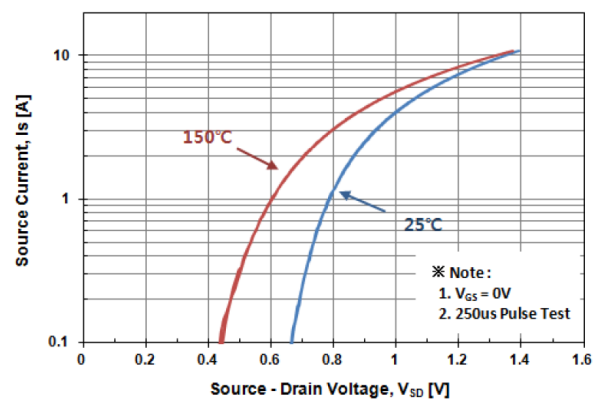


Fig. 5 Typical Capacitance Characteristics

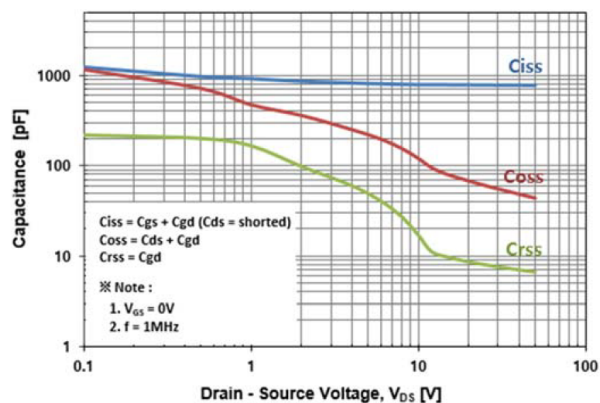


Fig. 6 Typical Total Gate Charge Characteristics

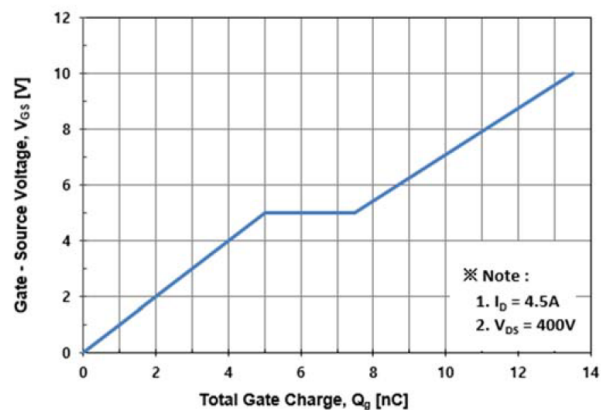


Fig. 7 Breakdown Voltage Variation vs. Temperature

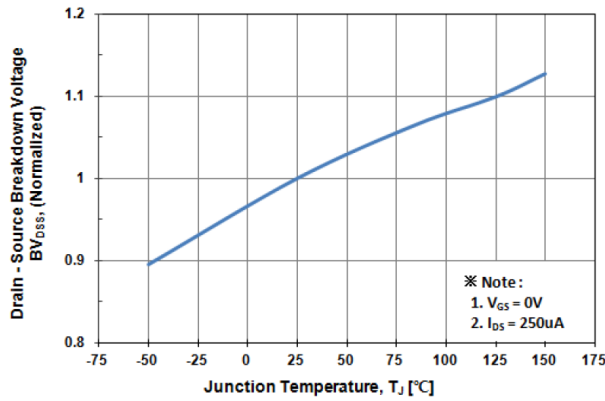


Fig. 8 On-Resistance Variation vs. Temperature

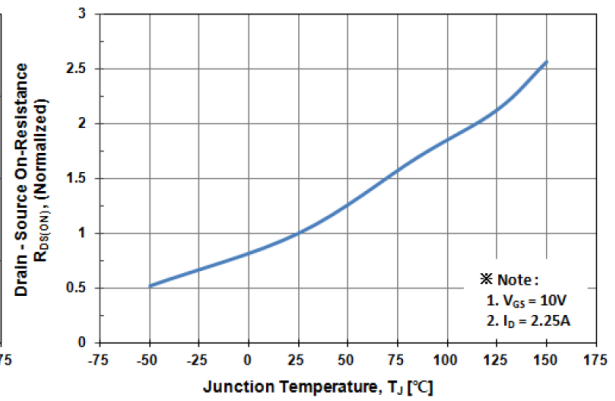


Fig. 9 Maximum Drain Current vs. Case Temperature

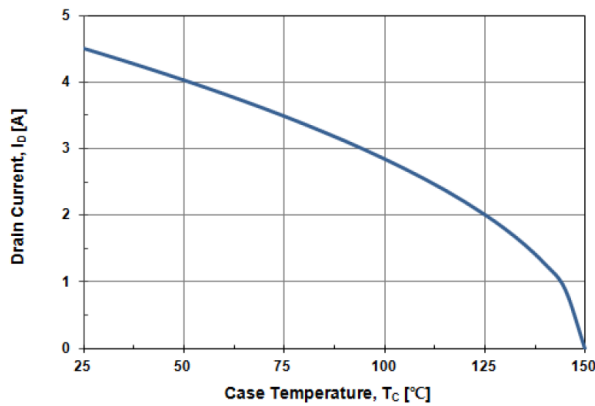


Fig. 10 Maximum Safe Operating Area

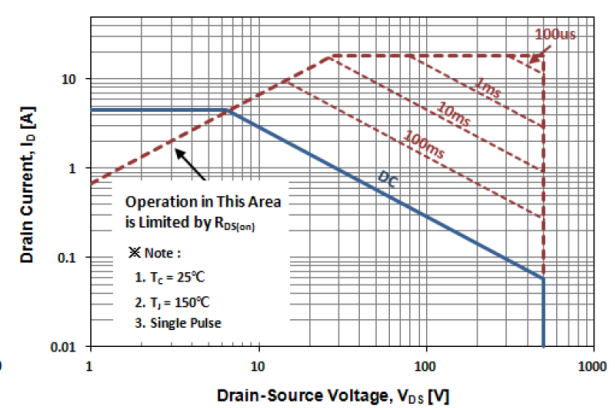


Fig. 11 Transient Thermal Impedance

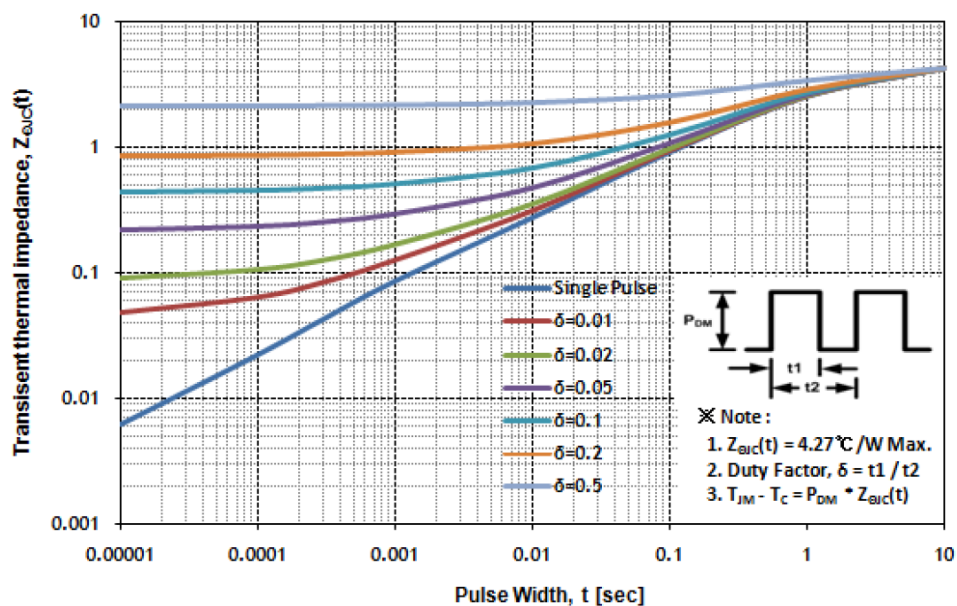


Fig. 12 Gate Charge Test Circuit & Waveform

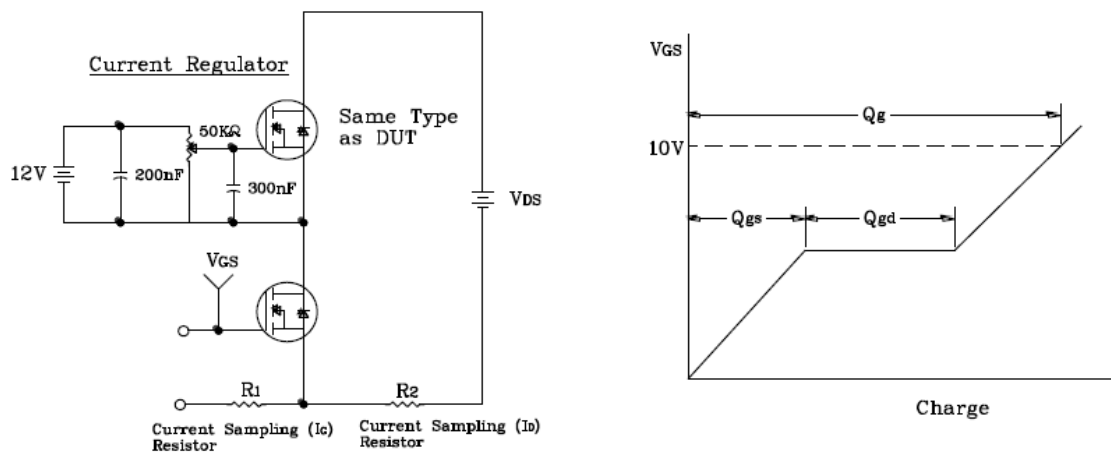


Fig. 13 Resistive Switching Test Circuit & Waveform

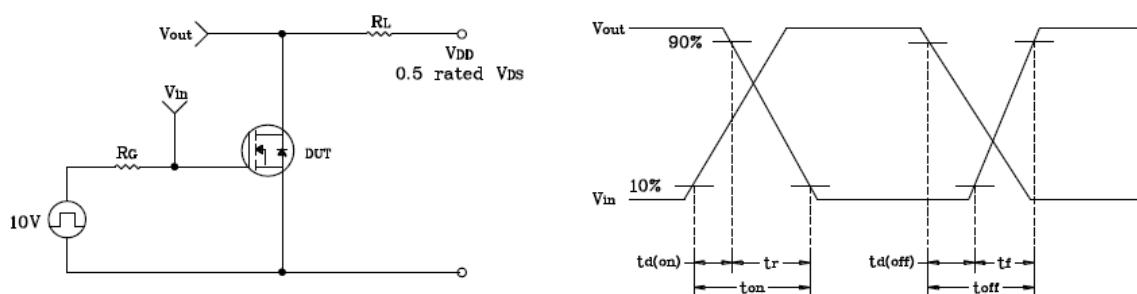


Fig. 14 E_{AS} Test Circuit & Waveform

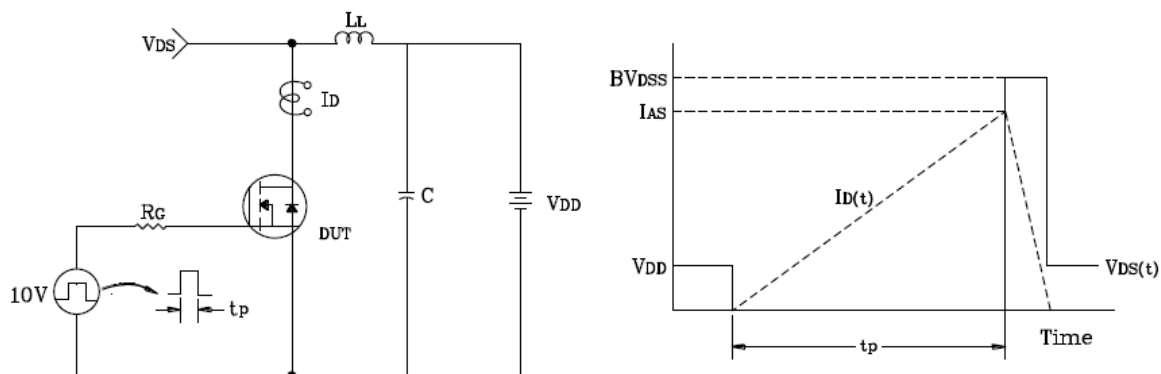
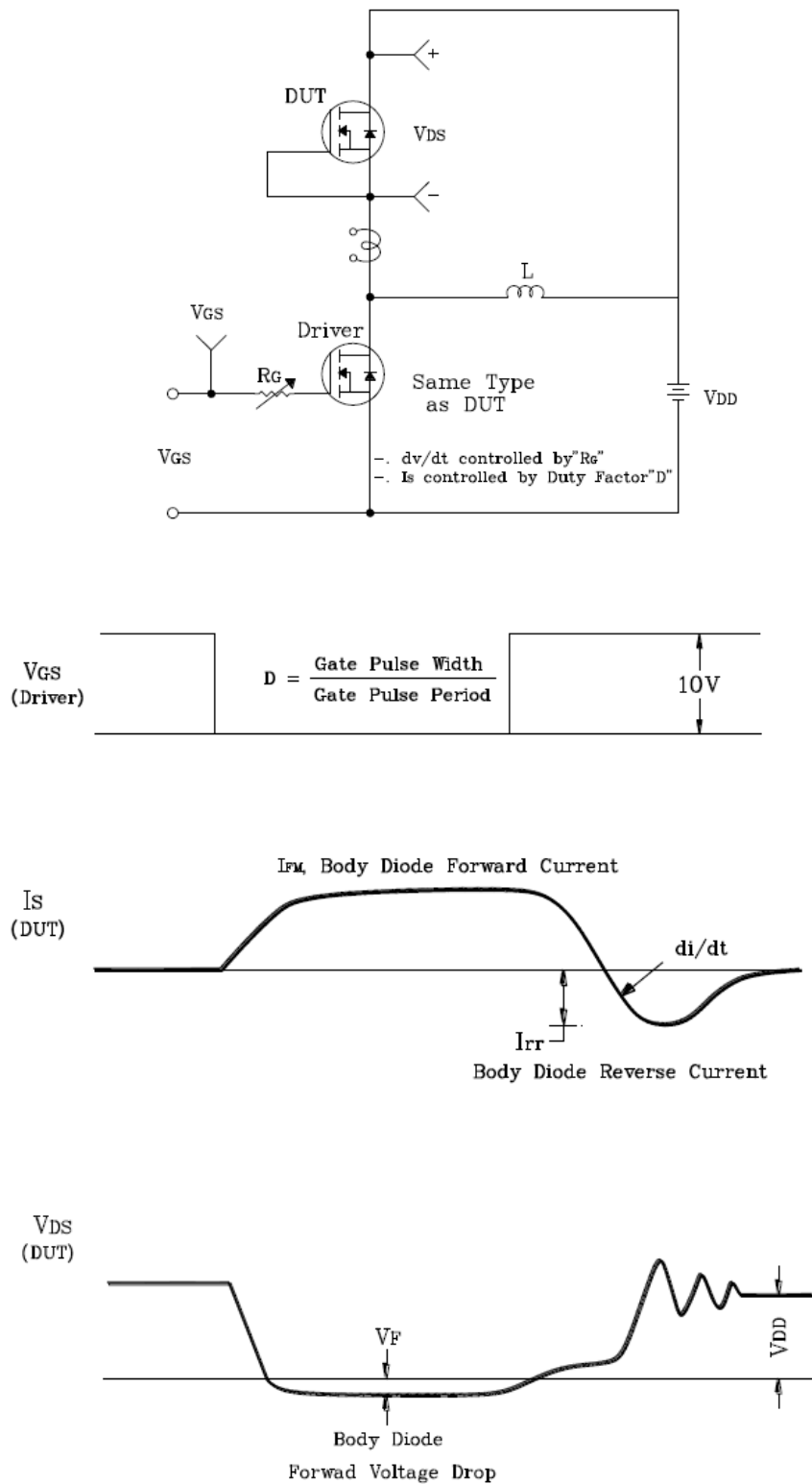


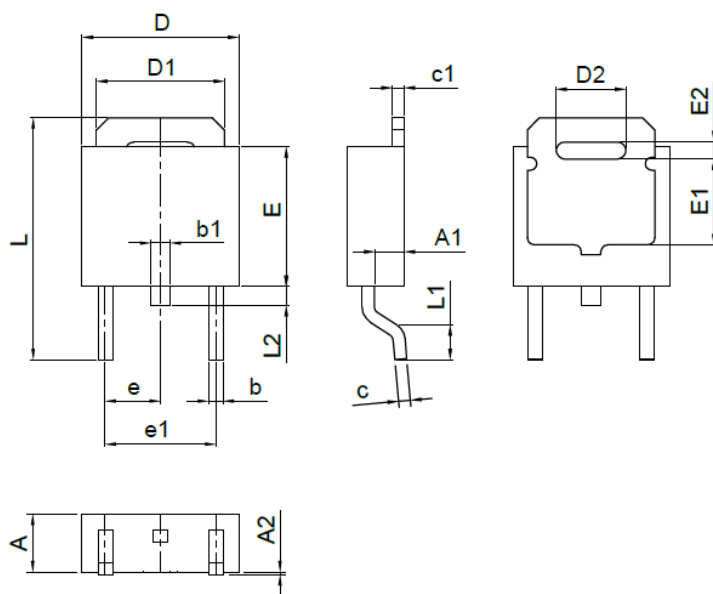


Fig. 15 Diode Reverse Recovery Time Test Circuit & Waveform



Package Dimensions

TO-252



Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.50	0.087	0.094
A1	1.00	1.40	0.039	0.055
A2	0.00	0.15	0.000	0.006
b	0.50	0.70	0.020	0.028
b1	0.70	0.90	0.028	0.035
c	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.20	6.70	0.244	0.264
D1	5.10	5.50	0.201	0.217
D2	2.70	3.20	0.106	0.126
E	5.50	6.00	0.217	0.236
E1	2.60	3.00	0.102	0.118
E2	0.60	0.80	0.024	0.315
e	2.20	2.40	0.087	0.094
e1	4.40	4.80	0.173	0.189
L	9.70	10.40	0.382	0.409
L1	1.40	1.70	0.055	0.063
L2	0.60	1.20	0.024	0.047

Revision History

No	Date	Contents
0	2022-12-10	Initial Brief Datasheet Release

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